

(use as many sheets as necessary)

**JAN 08 2006**

**Complete if Known**

Application Number	10/749,867
Filing Date	December 31, 2003
First Named Inventor	Yan Zhou
Group Art Unit	2646
Examiner Name	Singh, Ramnandan P.
Attorney Docket Number	75622P006201

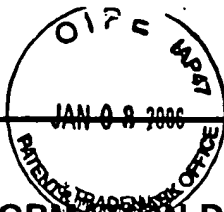
## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

**Examiner  
Signature**

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
Re		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX Global Enhanced Multiport Integrated ADSL Transceiver", Infineon Technologies AG, Germany (2001)	
Re		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX Global Enhanced Multiport Integrated ADSL Transceiver", Infineon Technologies AG, Germany (2001)	
Re		INFINEON TECHNOLOGIES, "Press Release: Infineon Technologies Introduces the IVAX Family - the First Chipset to Integrate Voice and Fullrate ADSL on a Single Line Card", Infineon Technologies AG, Germany (May 15, 2001)	
Re		PESSL, PETER, ET AL. "A four channel Integrated Voice and ADSL Full-Rate Analog Front End", 27th European Solid-State Circuits Conference, Villach, Austria, September 18-20, 2001	

Examiner  
Signature

Ram Singh

Date  
Considered

2/27/06

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†Unique citation designation number. ‡Applicant is to place a check mark here if English language Translation is attached.